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The Resume Builder

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WORK EXPERIENCE

SENIOR LAYOUT ENGINEER

02/2018 - PRESENT

Boston, MA

- Layout of IC designs for existing technologies (CMOS, BiCMOS, Bipolar etc)
- Evaluating layout requirements for designs targeting different technologies (analog or digital)
- Place & Route for digital designs
- Contributing to layout tool suites by supporting and/or developing CAD scripts (Memory cell placement, antenna effect mitigation, etc)
- Experience in design of high performance data converters and high speed circuits (GHz)
- Experience in designing a range of analog circuits such as amplifiers, oscillators, reference circuits and comparators
- Excellent understanding of process technology and the influence on circuit behavior
- Excellent understanding of circuit simulators and simulation techniques
- Ability to create innovate circuits and identify novel circuit techniques

LAYOUT ENGINEER

06/2014 - 10/2017

Los Angeles, CA

- Work with engineering to define variation requirements for geometric interfaces and ensure compliance for Static/Dynamic clearances, service, craftsmanship, assembly and manufacturing and total vehicle package targets
- Support GD&T creation, RSS calculations and print review
- Knowledge in device matching, balance, and shielding of critical device and signal
- Layout skills to finish tasks quickly and catches up on tight tape-out schedules if needed
- Decent understanding in the issues of electro-migration and IR drop, RC delay, self-heating, capacitive cross-talk, etc.
- Receives the input data for the layout engineering of the building (Process & Instrumentation Diagrams, equipment data, cable list, electrical load list, etc.), analyses them and verifies their technical consistency
- Carries out layout engineering or makes proposals to improve the layout - Lays out the various